

11. Package outline

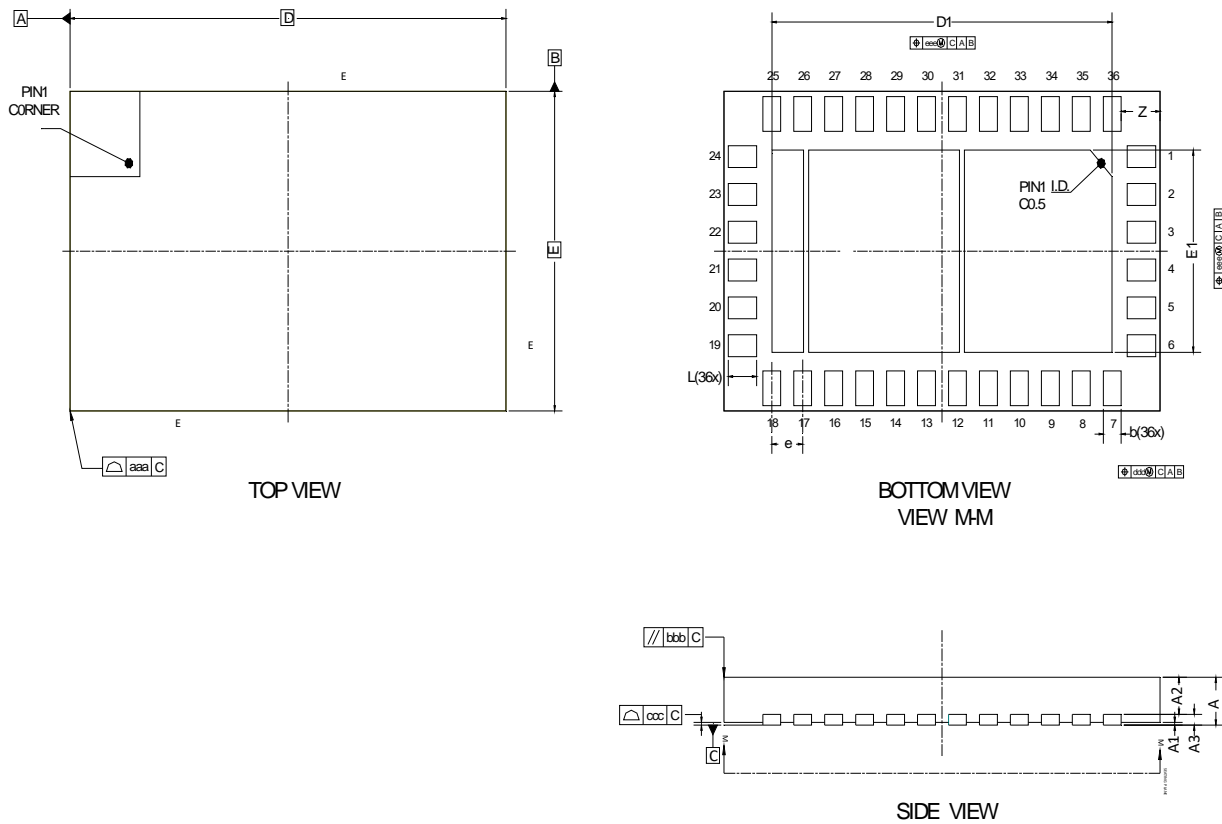


Fig 5. Package outline — LGA 6×10mm

Table 11. Package dimensions

DESCRIPTION	DIM	MILLIMETER			
		MIN	NOM	MAX	
TOTAL THICKNESS	A	0.85	0.90	0.95	
STAND OFF	A1	0.00	-----	0.05	
MOLD THICKNESS	A2	0.525	0.60	0.675	
L/F THICKNESS	A3	0.30 REF			
BODY SIZE	X	D	9.90	10.00	10.10
	Y	E	5.90	6.00	6.10
LEAD PITCH	e	0.71 BSC			
LEAD WIDTH	b	0.36	0.41	0.46	
LEAD LENGTH	L	0.60	0.65	0.70	
EP SIZE	X	D1	7.60	7.80	8.00
	Y	E1	3.70	3.80	3.90
LEAD EDGE TO PKG EDGE	Z	0.89 BSC			
Tolerance of form and position					
PACKAGE EDGE TOLERANCE	aaa	0.1			
MOLD FLATNESS	bbb	0.1			

DESCRIPTION	DIM	MILLIMETER		
		MIN	NOM	MAX
LEAD COPLANARITY	ccc		0.08	
LEAD POSITION OFFSET	ddd		0.1	
EXPOSED PAD OFFSET	eee		0.1	

12. Abbreviations

Table 12. Abbreviations

Acronym	Description
CW	Continuous Waveform
ESD	Electro-Static Discharge
GaN	Gallium Nitride
HEMT	High Electron Mobility Transistor
MTTF	Median Time To Failure
VSWR	Voltage Standing Wave Ratio

13. Legal information

13.1 Datasheet status

Document status	Product status	Definition
Objective [short] datasheet	Engineering sample	This document contains data from the objective specification for product development.
Preliminary [short] datasheet	Engineering sample	This document contains data from the preliminary specification.
Production [short] datasheet	Mass product	This document contains the product specification.

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